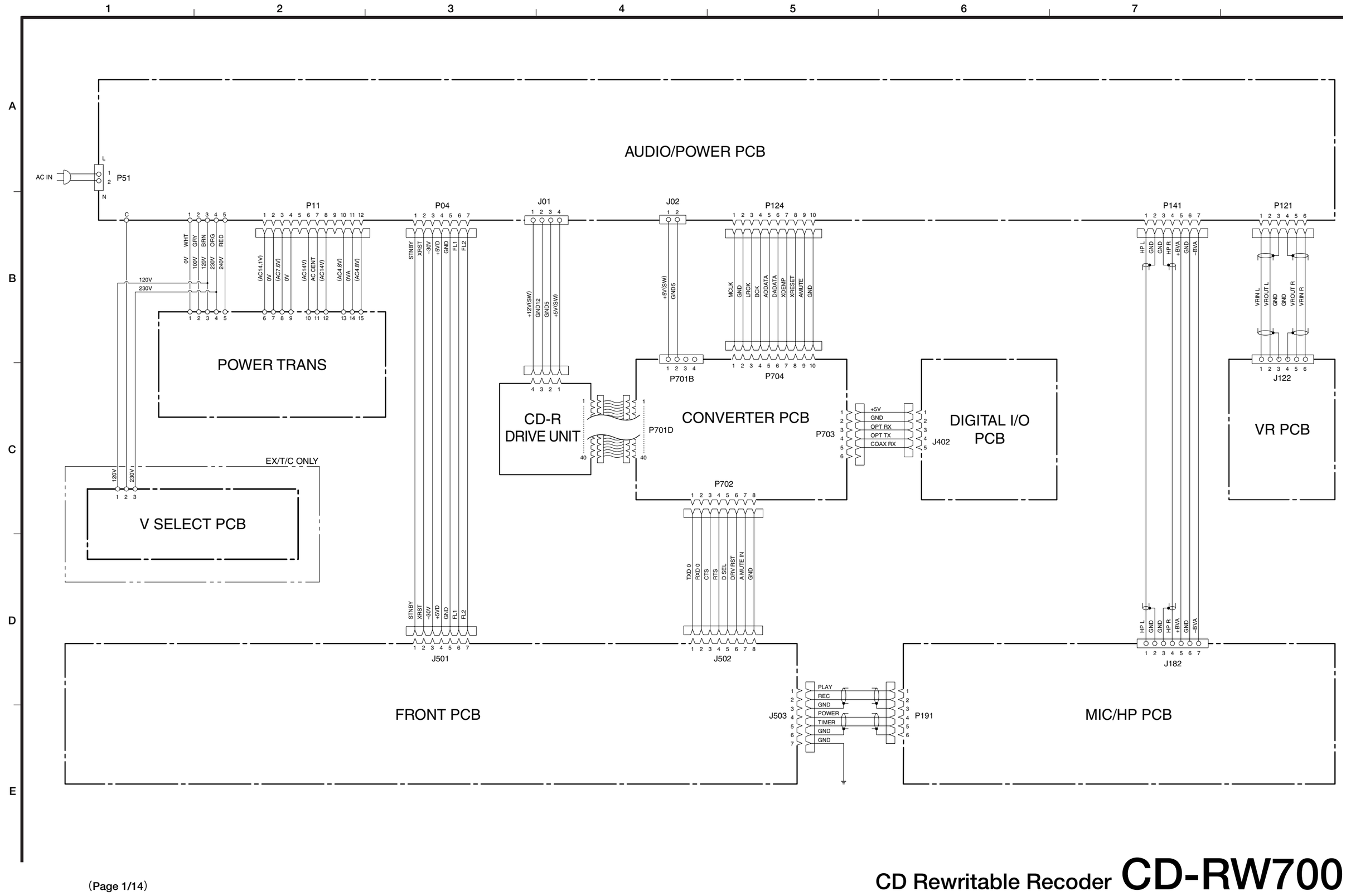
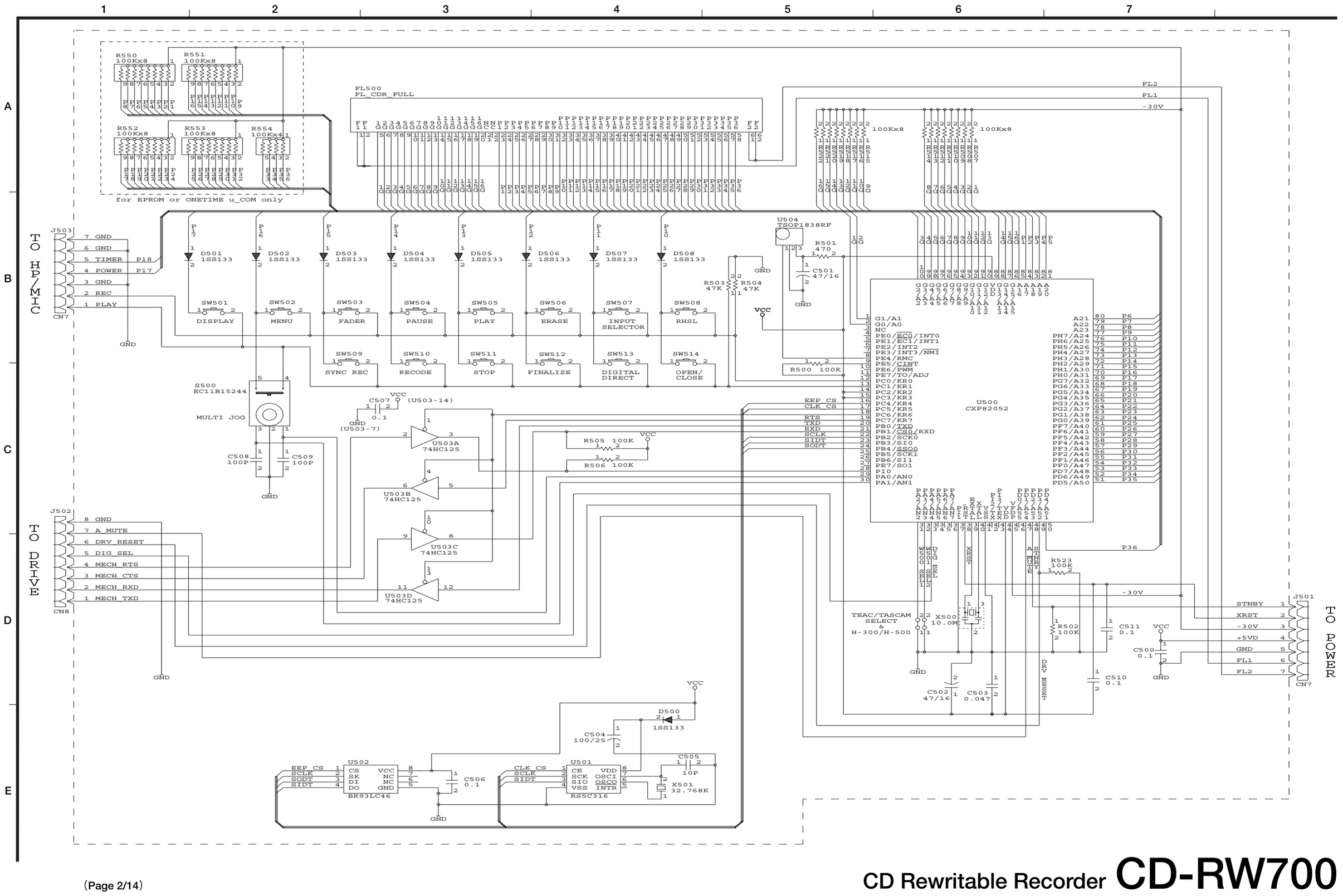
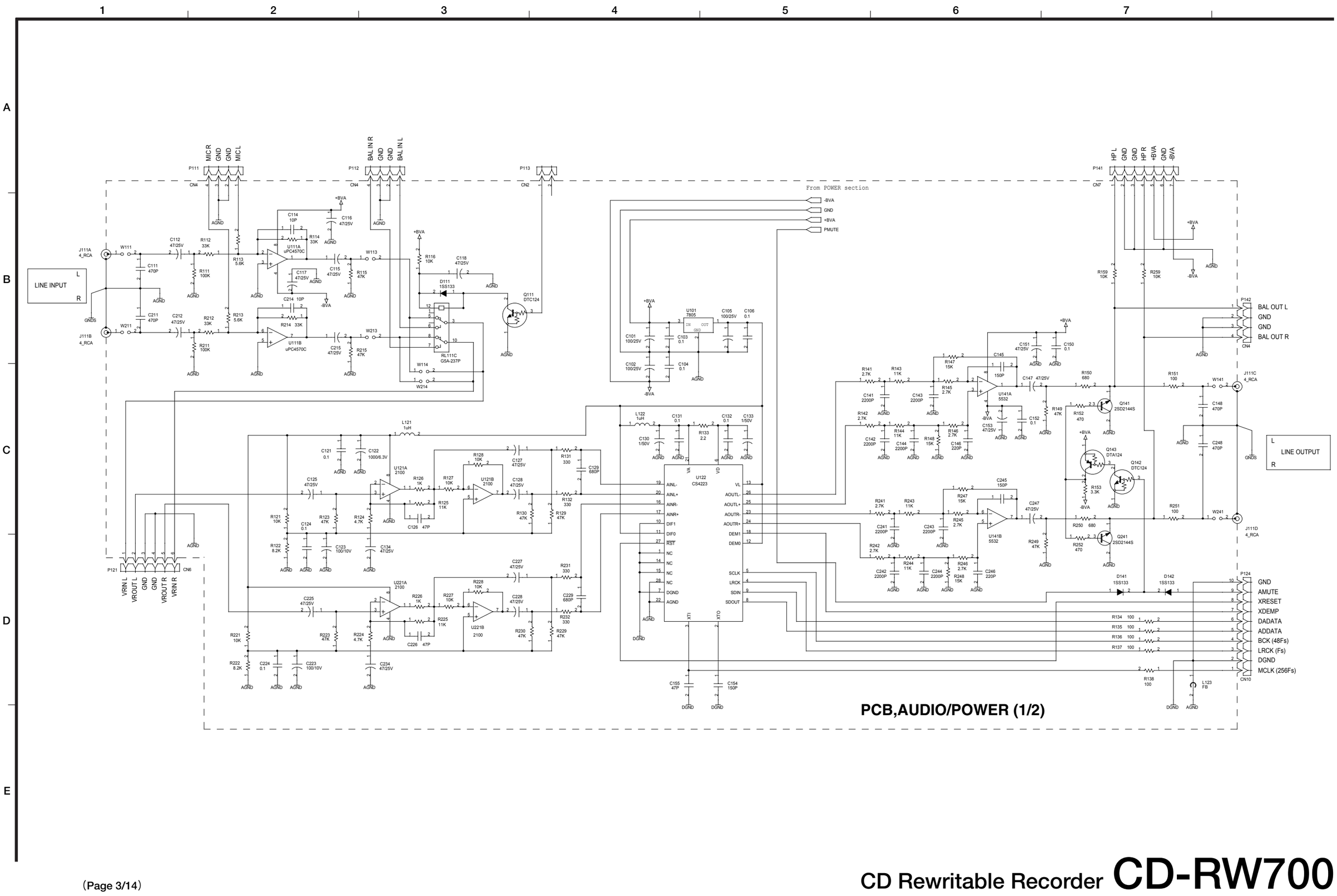
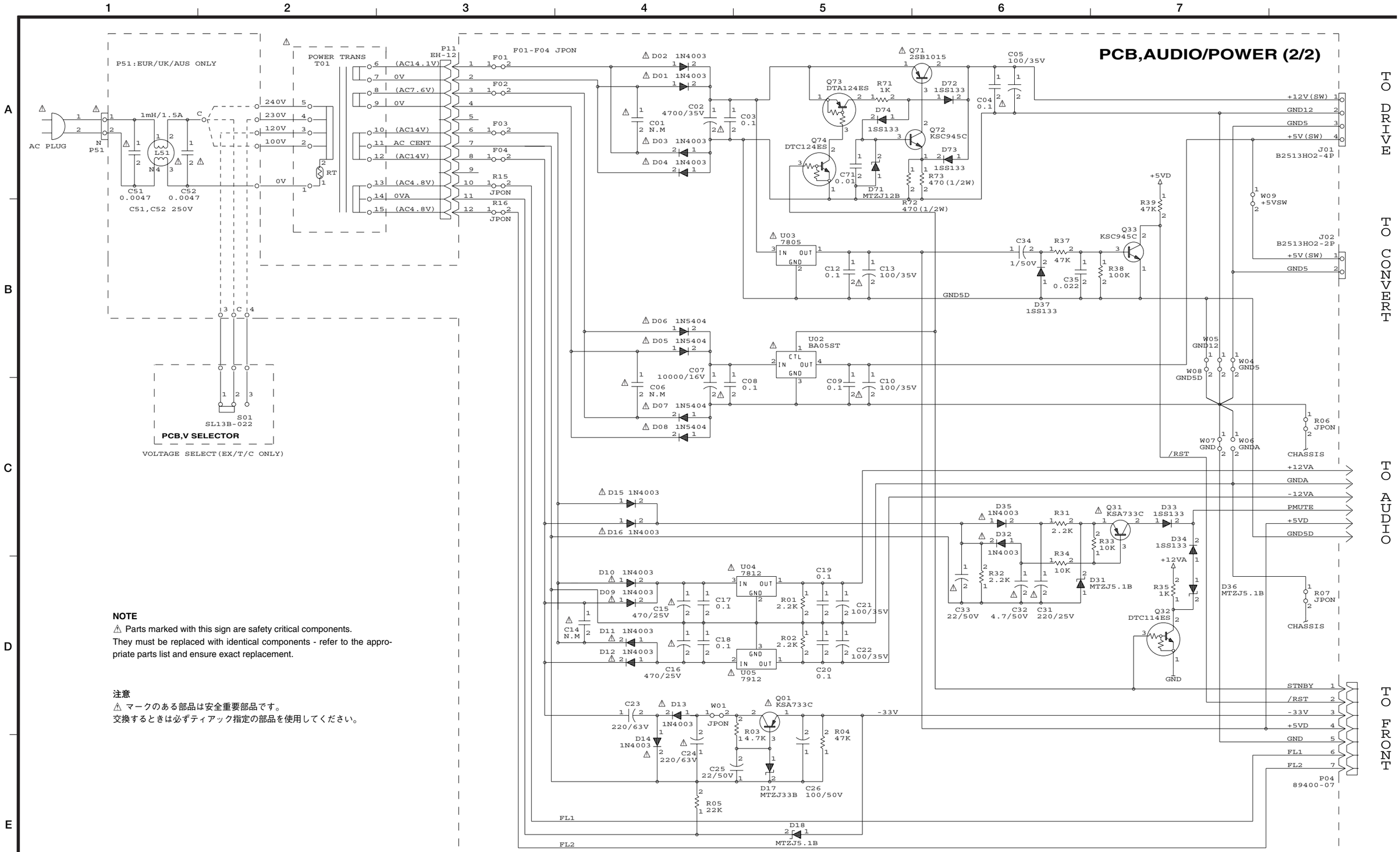


CD Rewritable Recoder **CD-RW700**


1
2
3
4
5
6
7
A
B
C
D
E
R550 100Kx8
R551 100Kx8
R552 100Kx8
R553 100Kx8
R554 100Kx4
for EPROM or ONETIME u_COM only
FL500
FL_CDR_FULL
FL2
FL1
-30V
100Kx8
100Kx8
TO HP/MIC
J503
7 GND
6 GND
5 TIMER P18
4 POWER P17
3 GND
2 REC
1 PLAY
CN7
GND
D501 1SS133
D502 1SS133
D503 1SS133
D504 1SS133
D505 1SS133
D506 1SS133
D507 1SS133
D508 1SS133
SW501 DISPLAY
SW502 MENU
SW503 FADER
SW504 PAUSE
SW505 PLAY
SW506 ERASE
SW507 INPUT SELECTOR
SW508 RHSL
SW509 SYNC REC
SW510 RECODE
SW511 STOP
SW512 FINALIZE
SW513 DIGITAL DIRECT
SW514 OPEN/ CLOSE
U504 TSOP1838RF
R501 470
C501 47/16
R503 47K
R504 47K
VCC
GND
R500 100K
S500 EC11B15244
MULTI JOG
C508 100P
C509 100P
C507 0.1
VCC (U503-14)
GND (U503-7)
U503A 74HC125
U503B 74HC125
U503C 74HC125
U503D 74HC125
R505 100K
R506 100K
U500 CXP82052
EEP_CS
CLK_CS
RTS
TXD
RXD
SCLK
SIDT
SODT
P36
TO DRIVE
J502
8 GND
7 A_MUTE
6 DRV_RESET
5 DIG_SEL
4 MECH_RTS
3 MECH_CTS
2 MECH_RXD
1 MECH_TXD
CN8
R523 100K
-30V
TEAC/TASCAM SELECT & H-300/H-500
X500 10.0M
R502 100K
C511 0.1
C510 0.1
C500 0.1
C502 47/16
C503 0.047
J501
STNBY 1
XRST 2
-30V 3
+5VD 4
GND 5
FL1 6
FL2 7
CN7
TO POWER
D500 1SS133
C504 100/25
U502 BR93LC46
U501 RS5C316
C506 0.1
C505 10P
X501 32.768K

SCHEMATIC DIAGRAM CD-RW700 AUDIO/POWER PCB (1/2)



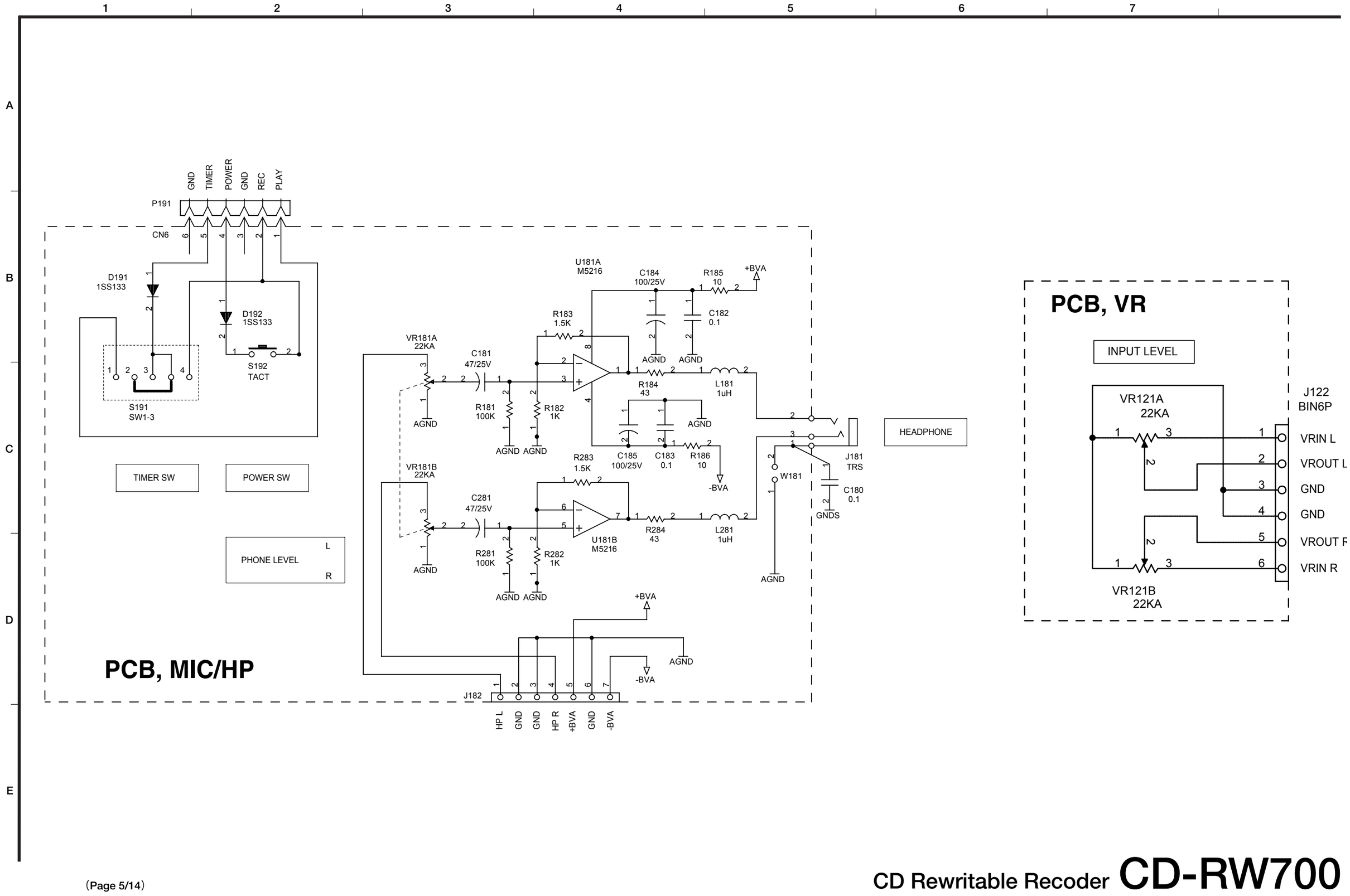


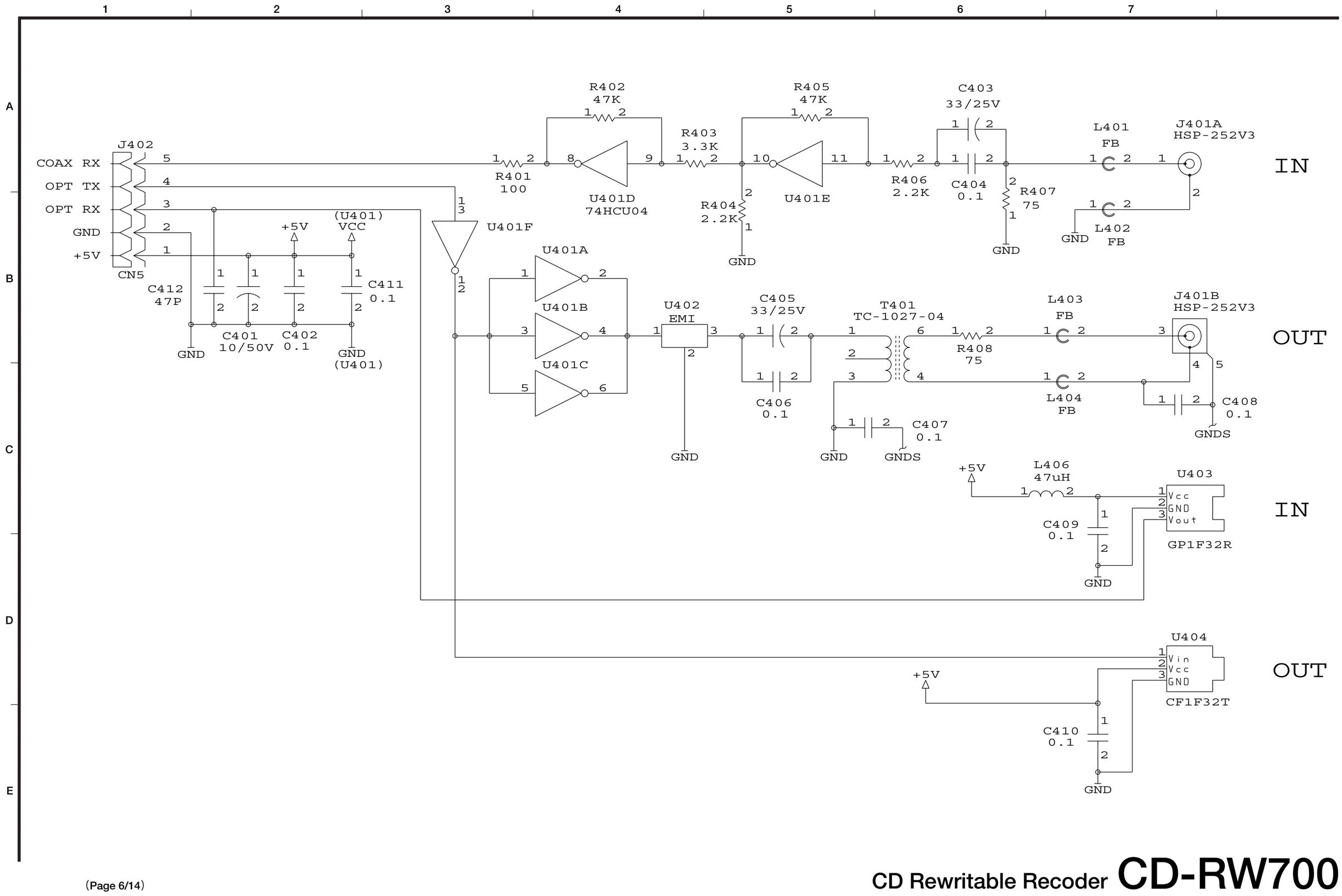
NOTE

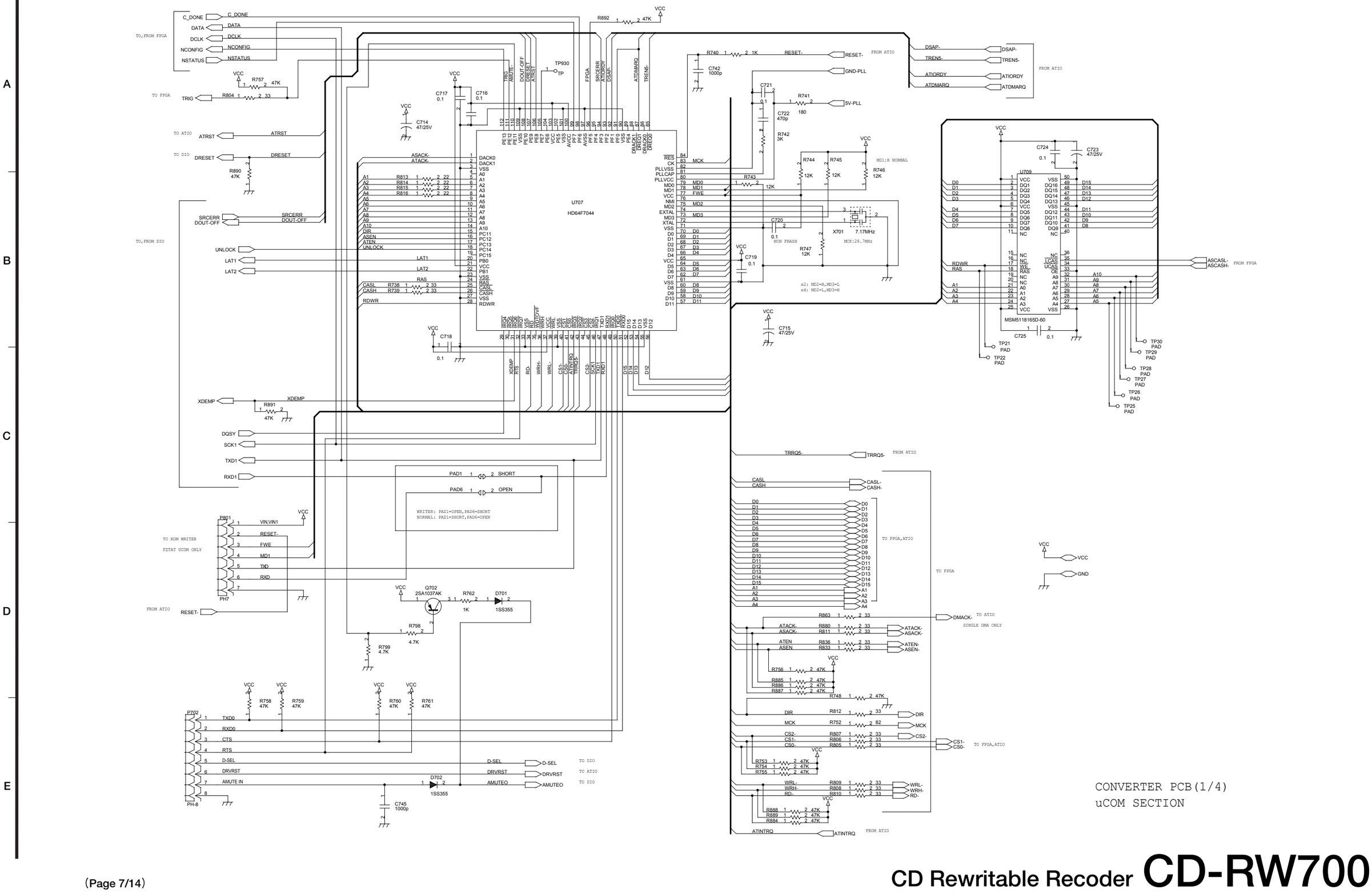
⚠ Parts marked with this sign are safety critical components.
They must be replaced with identical components - refer to the appropriate parts list and ensure exact replacement.

注意

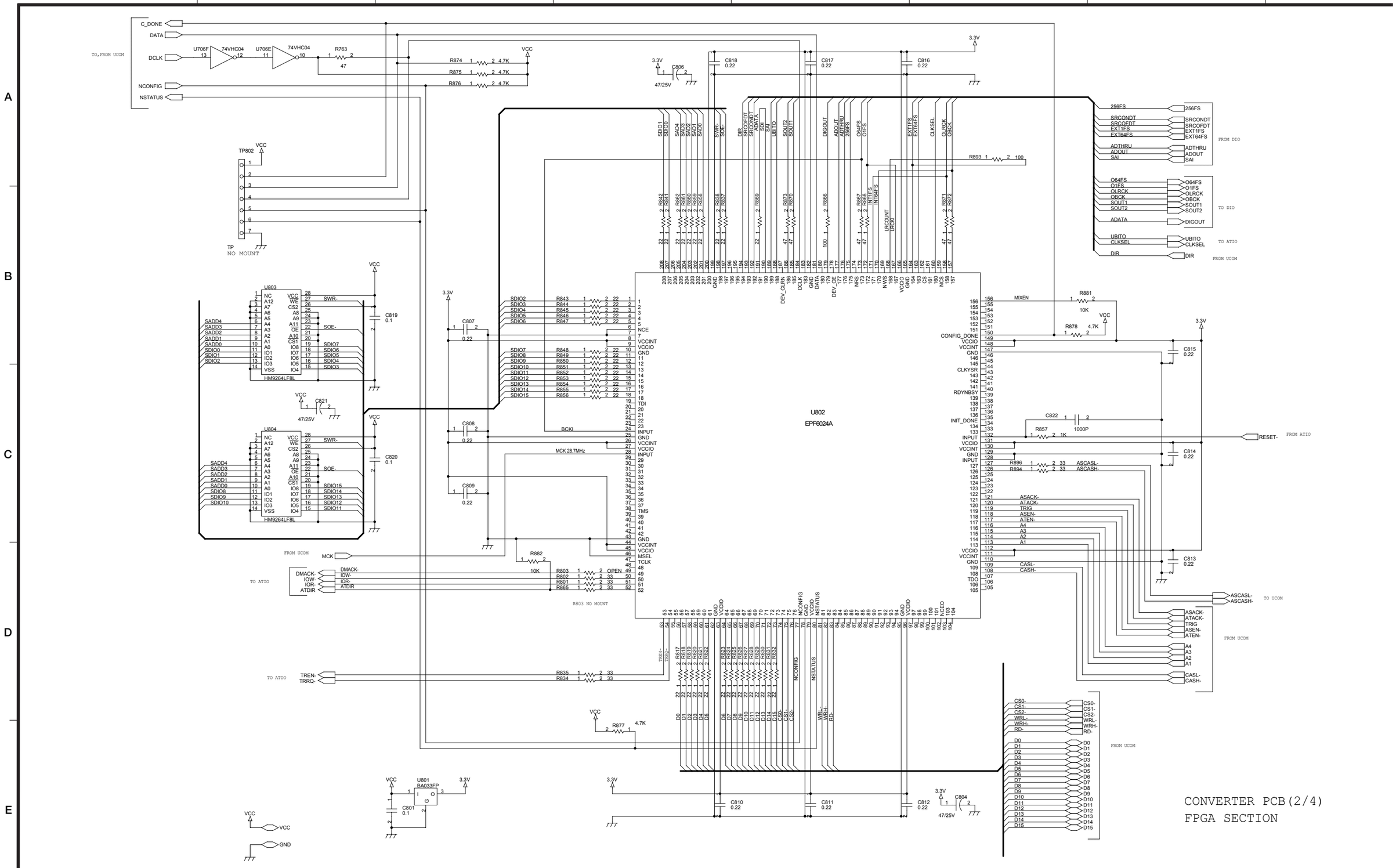
⚠ マークのある部品は安全重要部品です。
交換するときは必ずティアック指定の部品を使用してください。


1
2
3
4
5
6
7
A
B
C
D
E
P191
GND
TIMER
POWER
GND
REC
PLAY
CN6
D191
1SS133
D192
1SS133
S191
SW1-3
S192
TACT
TIMER SW
POWER SW
PHONE LEVEL
L
R
PCB, MIC/HP
VR181A
22KA
VR181B
22KA
C181
47/25V
C281
47/25V
R181
100K
R281
100K
R182
1K
R282
1K
R183
1.5K
R283
1.5K
U181A
M5216
U181B
M5216
C184
100/25V
C185
100/25V
C182
0.1
C183
0.1
R185
10
R186
10
R184
43
R284
43
L181
1uH
L281
1uH
+BVA
-BVA
AGND
J182
HP L
GND
GND
HP R
+BVA
GND
-BVA
HEADPHONE
J181
TRS
W181
C180
0.1
GNDS
PCB, VR
INPUT LEVEL
VR121A
22KA
VR121B
22KA
J122
BIN6P
VRIN L
VROUT L
GND
GND
VROUT R
VRIN R

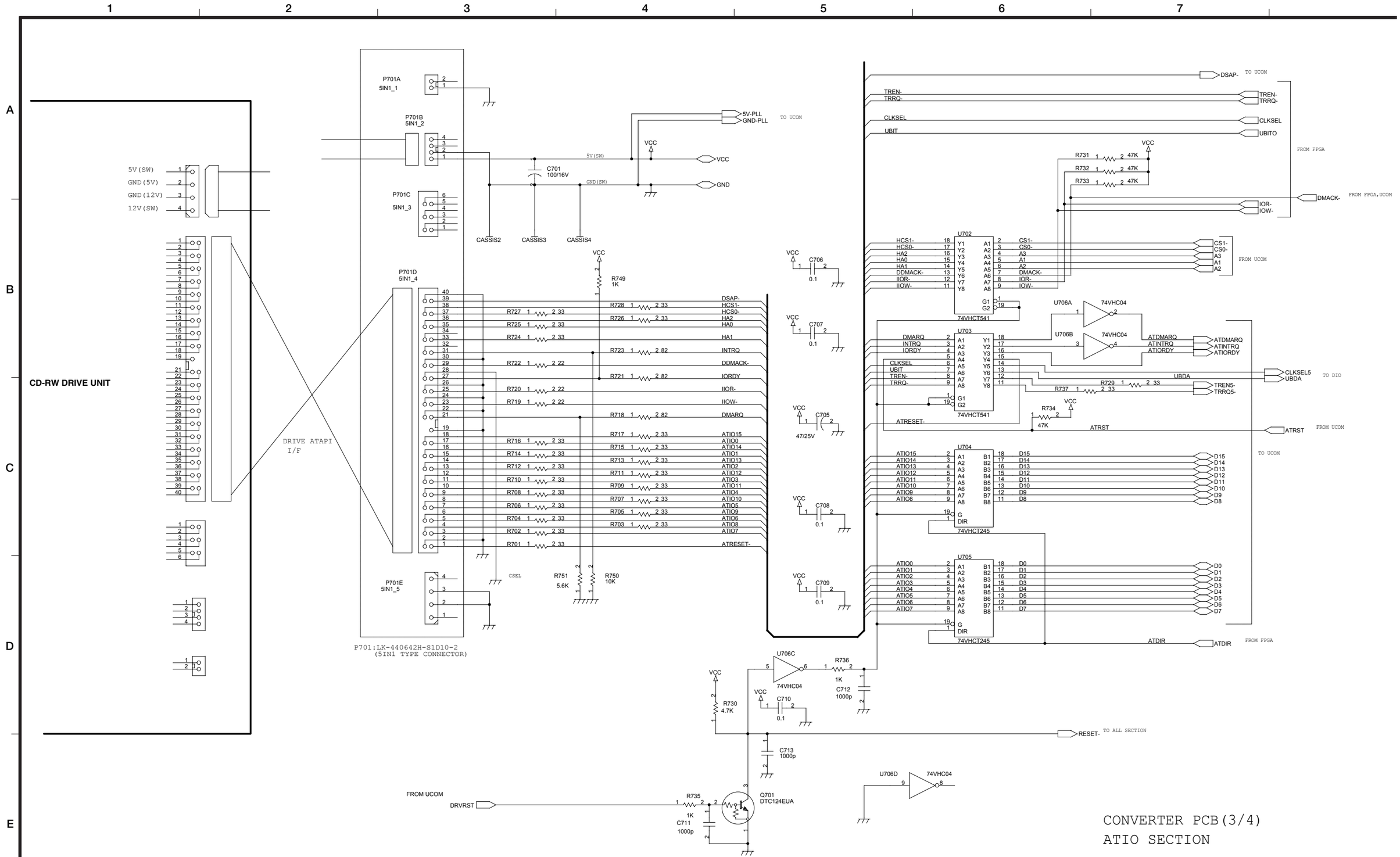

1
2
3
4
5
6
7
A
B
C
D
E
J402
COAX RX
OPT TX
OPT RX
GND
+5V
CN5
C412
47P
C401
10/50V
C402
0.1
C411
0.1
GND
GND
(U401)
+5V
(U401)
VCC
R401
100
R402
47K
U401D
74HCU04
R403
3.3K
R404
2.2K
R405
47K
U401E
R406
2.2K
C404
0.1
R407
75
C403
33/25V
L401
FB
L402
FB
J401A
HSP-252V3
IN
U401F
U401A
U401B
U401C
U402
EMI
C405
33/25V
C406
0.1
T401
TC-1027-04
R408
75
L403
FB
L404
FB
J401B
HSP-252V3
C407
0.1
C408
0.1
GNDS
OUT
+5V
L406
47uH
U403
Vcc
GND
Vout
GP1F32R
C409
0.1
IN
U404
Vin
Vcc
GND
CF1F32T
C410
0.1
OUT


C_DONE
DATA
DCLK
NCONFIG
NSTATUS
TO,FROM FPGA
TRIG
ATRST
DRESET
SRCERR
DOUT-OFF
UNLOCK
LAT1
LAT2
RAS
CASL
CASH
RDWR
XDEMP
DQSY
SCK1
TXD1
RXD1
PAD1 SHORT
PAD6 OPEN
WRITER: PAD1=OPEN,PAD6=SHORT
NORMAL: PAD1=SHORT,PAD6=OPEN
P801
VIN,VIN1
RESET-
FWE
MD1
TXD
RXD
PH7
TO ROM WRITER
FITAT UCOM ONLY
FROM ATIO
P702
TXD0
RXD0
CTS
RTS
D-SEL
DRVRST
AMUTE IN
PH-8
D702
1SS355
C745
1000p
Q702
2SA1037AK
R762
1K
D701
1SS355
R798
4.7K
R799
4.7K
U707
HD64F7044
TP930
TP
R892
47K
C717
0.1
C716
0.1
C714
47/25V
R757
47K
R804
33
R890
47K
R813
R814
R815
R816
22
R738
R739
33
C718
0.1
R891
47K
R758
R759
R760
R761
47K
TRIG
AMUTE-
DOUT-OFF
DRESET
ATRST
FPGA
SRCERR
ATIORDY
DSAP-
ATDMARQ
TREN5-
R740
1K
RESET-
C742
1000p
GND-PLL
C721
0.1
R741
180
5V-PLL
C722
470p
R742
3K
R744
R745
R746
12K
MD1:H NORMAL
R743
12K
C720
0.1
NON FRASH
X701
7.17MHz
MCK:28.7MHz
R747
12K
x2: MD2=H,MD3=L
x4: MD2=L,MD3=H
C719
0.1
C715
47/25V
U709
MSM5118165D-60
C724
0.1
C723
47/25V
C725
0.1
RDWR
RAS
TP21 PAD
TP22 PAD
TP30 PAD
TP29 PAD
TP28 PAD
TP27 PAD
TP26 PAD
TP25 PAD
ASCASL-
ASCASH-
FROM FPGA
DSAP-
TREN5-
ATIORDY
ATDMARQ
FROM ATIO
TRRQ5-
TO FPGA,ATIO
TO FPGA
R863
DMACK-
TO ATIO
SINGLE DMA ONLY
ATACK-
ASACK-
ATEN-
ASEN-
R756
R885
R886
R887
R748
DIR
R812
MCK
R752
CS2-
R807
CS1-
R806
CS0-
R805
R753
R754
R755
WRL-
R809
WRH-
R808
RD-
R810
R888
R889
R884
ATINTRQ
FROM ATIO
VCC
GND
CONVERTER PCB(1/4)
uCOM SECTION

SCHEMATIC DIAGRAM CD-RW700 CONVERTER PCB (3E9024700B) (2/4)

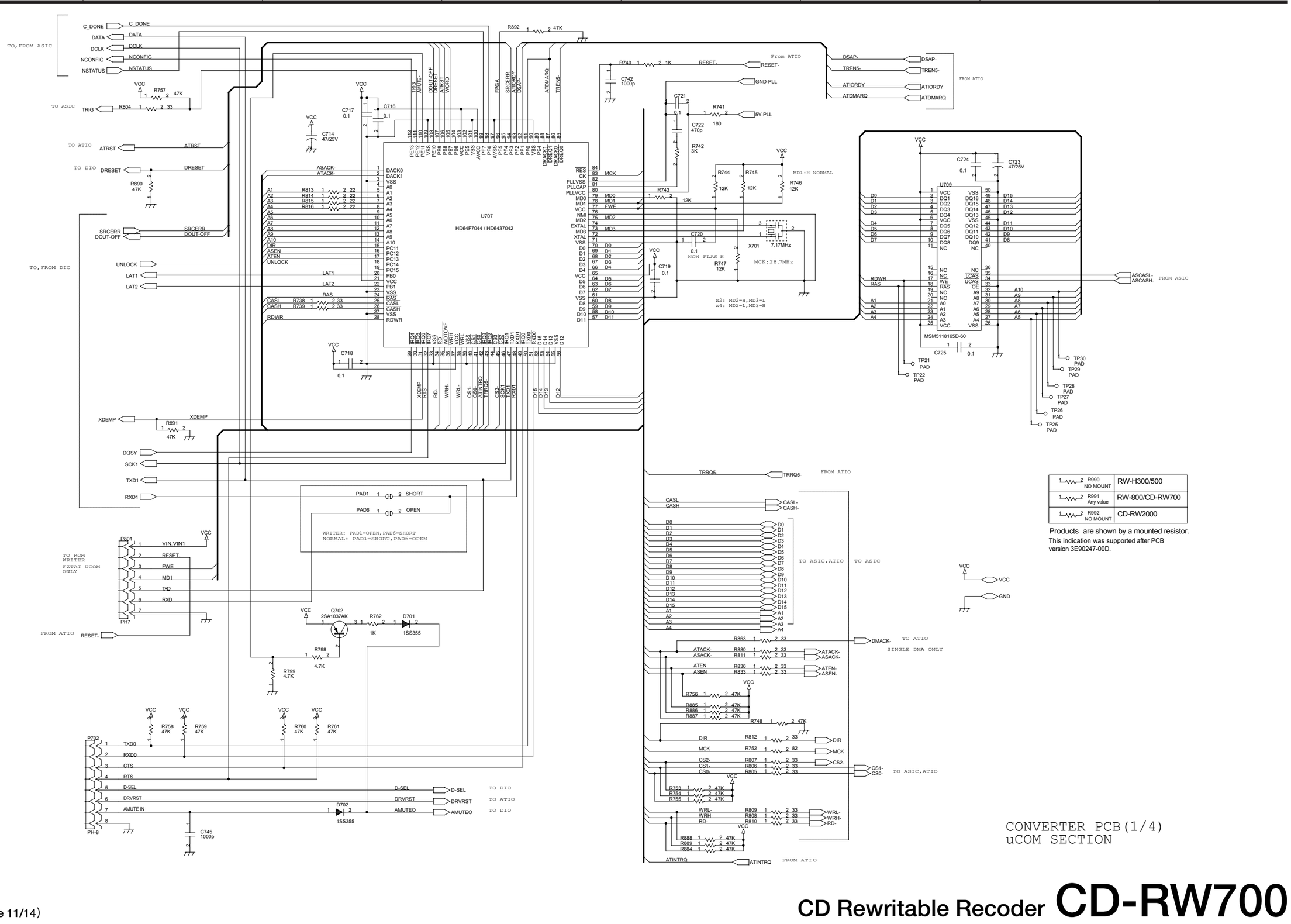


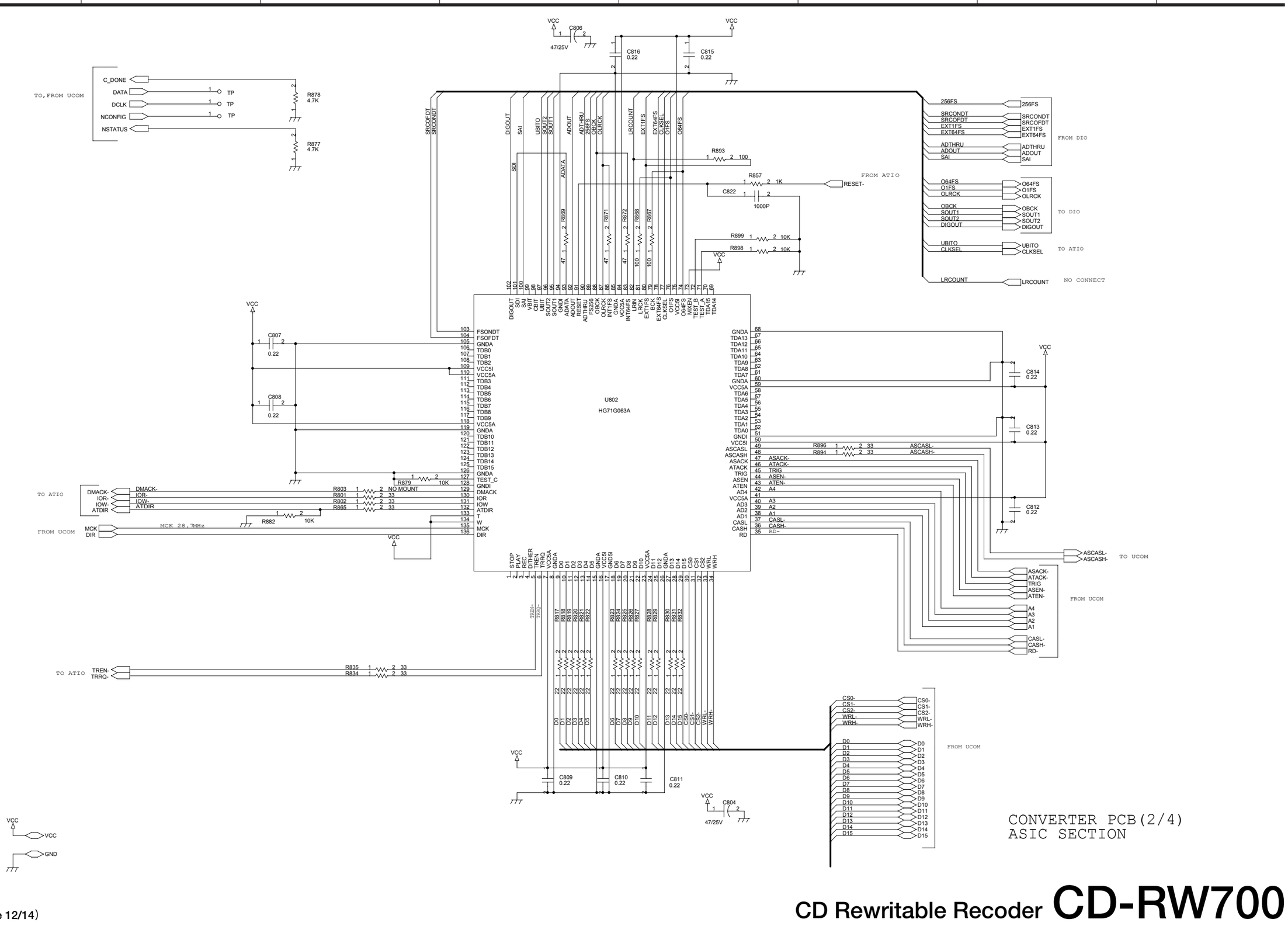
SCHEMATIC DIAGRAM CD-RW700 CONVERTER PCB (3E9024700B) (3/4)



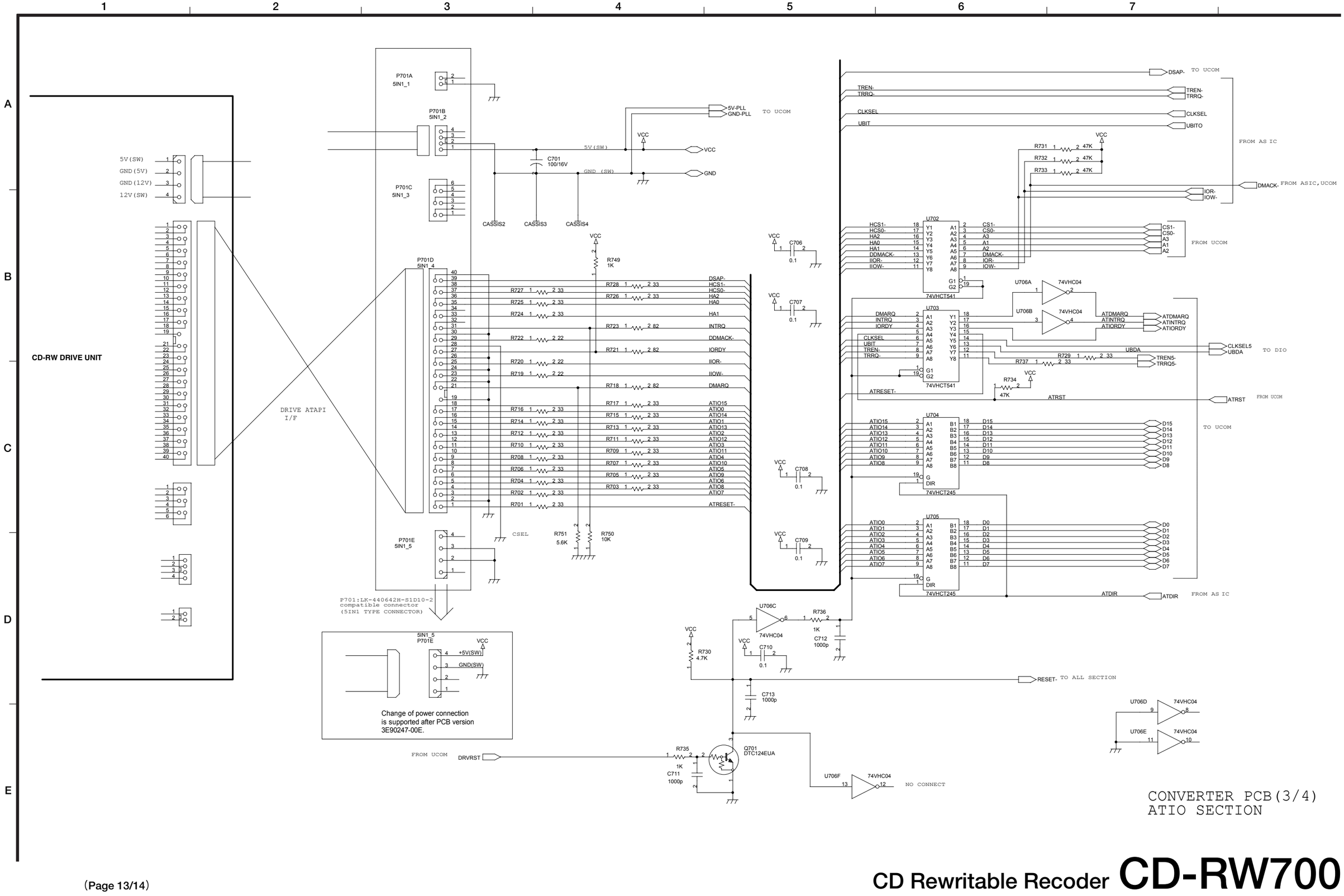
CONVERTER PCB(4/4)
DIO SECTION

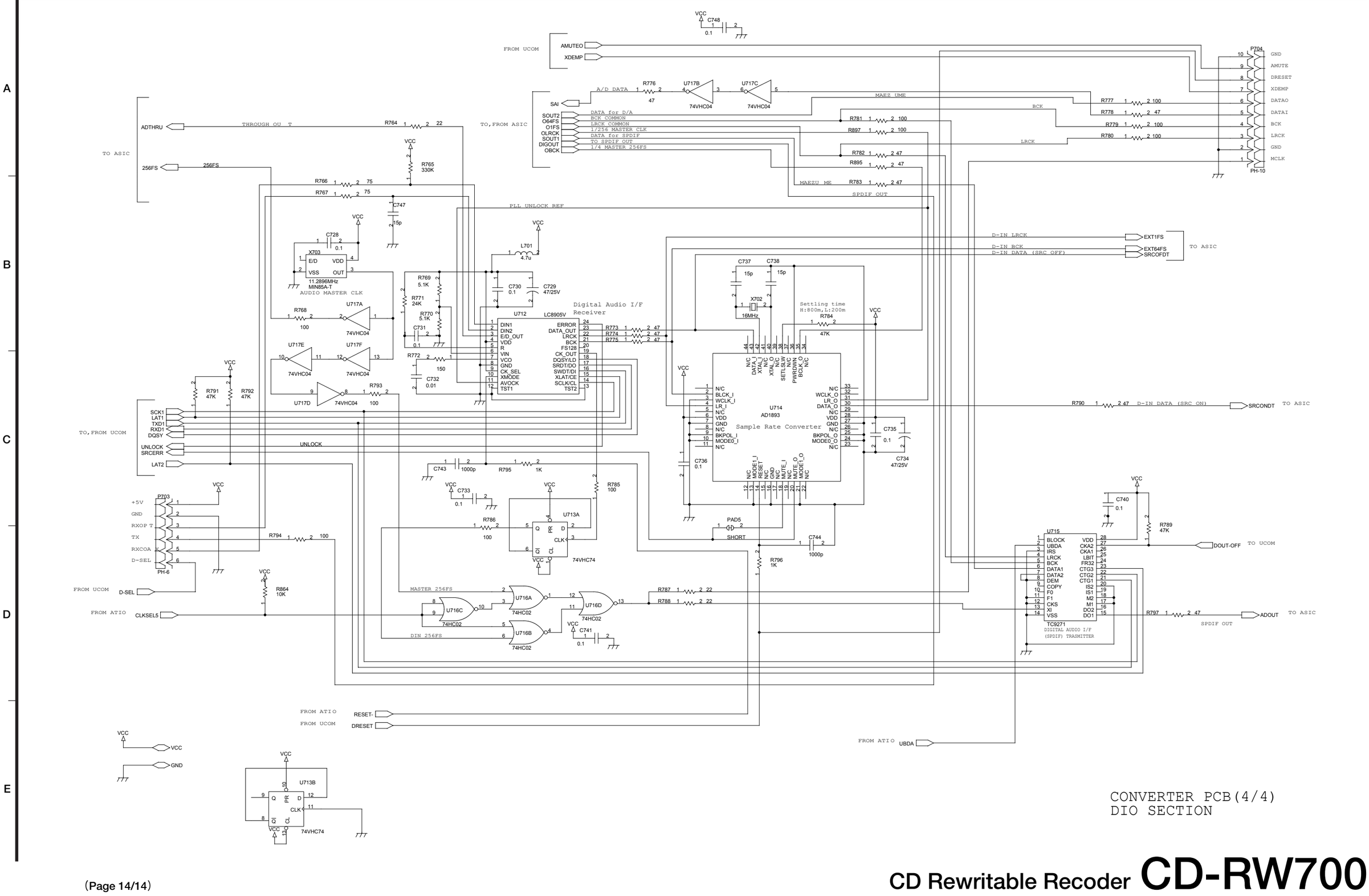
SCHEMATIC DIAGRAM CD-RW700 CONVERTER PCB (3E9024700D) (1/4)




TO,FROM UCOM
C_DONE
DATA
DCLK
NCONFIG
NSTATUS
TP
R878 4.7K
R877 4.7K
VCC
C806 47/25V
C816 0.22
C815 0.22
SRCOFDT
SRCONDT
DIGOUT
SAI
SDI
UBITO
SOUT2
SOUT1
ADOUT
ADATA
ADTHRU
256FS
OBCK
OLRCK
LRCOUNT
EXT1FS
EXT64FS
CLKSEL
O1FS
O64FS
R893 100
R857 1K
RESET-
C822 1000P
FROM ATIO
R869
R871
R872
R868
R867
R899 10K
R898 10K
256FS
SRCONDT
SRCOFDT
EXT1FS
EXT64FS
ADTHRU
ADOUT
SAI
FROM DIO
O64FS
O1FS
OLRCK
OBCK
SOUT1
SOUT2
DIGOUT
TO DIO
UBITO
CLKSEL
TO ATIO
LRCOUNT
NO CONNECT
C807 0.22
C808 0.22
FSONDT
FSOFDT
TDB0
TDB1
TDB2
TDB3
TDB4
TDB5
TDB6
TDB7
TDB8
TDB9
TDB10
TDB11
TDB12
TDB13
TDB14
TDB15
TEST_C
DMCK
IOR
IOW
ATDIR
U802
HG71G063A
R879 10K
R803 NO MOUNT
R801 33
R802 33
R865 33
R882 10K
TO ATIO
DMACK-
IOR-
IOW-
ATDIR
FROM UCOM
MCK
DIR
MCK 28.7MHz
STOP
PLAY
REC
DITHER
TREN
TRRQ
R817
R818
R819
R820
R821
R822
R823
R824
R825
R826
R827
R828
R829
R830
R831
R832
TREN-
TRRQ-
TO ATIO
R835 33
R834 33
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
CS0-
CS1-
CS2-
WRL-
WRH-
FROM UCOM
GNDA
TDA13
TDA12
TDA11
TDA10
TDA9
TDA8
TDA7
TDA6
TDA5
TDA4
TDA3
TDA2
TDA1
TDA0
GNDI
VCC5I
ASCASL
ASCASH
ASACK
ATACK
TRIG
ASEN
ATEN
AD4
AD3
AD2
AD1
CASL
CASH
RD
C814 0.22
C813 0.22
C812 0.22
R896 33
R894 33
ASCASL-
ASCASH-
ASACK-
ATACK-
TRIG
ASEN-
ATEN-
A4
A3
A2
A1
CASL-
CASH-
RD-
ASCASL-
ASCASH-
TO UCOM
FROM UCOM
C809 0.22
C810 0.22
C811 0.22
C804 47/25V
VCC
GND
CONVERTER PCB(2/4)
ASIC SECTION

SCHEMATIC DIAGRAM CD-RW700 CONVERTER PCB (3E9024700D) (3/4)




1
2
3
4
5
6
7
A
B
C
D
E
VCC
C748 0.1
FROM UCOM
AMUTEO
XDEMP
A/D DATA
R776 47
U717B 74VHC04
U717C 74VHC04
SAI
SOUT2
O64FS
O1FS
OLRCK
SOUT1
DIGOUT
OBCK
DATA for D/A
BCK COMMON
LRCK COMMON
1/256 MASTER CLK
DATA for SPDIF
TO SPDIF OUT
1/4 MASTER 256FS
TO,FROM ASIC
MAEZ UME
MAEZU ME
SPDIF OUT
BCK
LRCK
R781 100
R897 100
R782 47
R895 47
R783 47
R777 100
R778 47
R779 100
R780 100
P704
GND
AMUTE
DRESET
XDEMP
DATAO
DATAI
BCK
LRCK
GND
MCLK
PH-10
ADTHRU
THROUGH OU T
R764 22
TO ASIC
256FS
R765 330K
R766 75
R767 75
C747 15p
PLL UNLOCK REF
C728 0.1
X703
E/D
VDD
VSS
OUT
11.2896MHz
MIN85A-T
AUDIO MASTER CLK
L701 4.7u
C730 0.1
C729 47/25V
R769 5.1K
R771 24K
R770 5.1K
C731 0.1
R772 150
C732 0.01
U712 LC8905V
Digital Audio I/F Receiver
DIN1
DIN2
E/D_OUT
VDD
R
VIN
VCO
GND
CK_SEL
AVOCK
TST1
ERROR
DATA_OUT
LRCK
BCK
FS128
CK_OUT
DQSY/LD
SRDT/DO
SWDT/DI
XLAT/CE
SCLK/CL
TST2
R773 47
R774 47
R775 47
D-IN LRCK
D-IN BCK
D-IN DATA (SRC OFF)
EXT1FS
EXT64FS
SRCOFDT
TO ASIC
C737 15p
C738 15p
X702 16MHz
Settling time
H:800m,L:200m
R784 47K
U768 100
U717A 74VHC04
U717E 74VHC04
U717F 74VHC04
U717D 74VHC04
R793 100
R791 47K
R792 47K
SCK1
LAT1
TXD1
RXD1
DQSY
TO,FROM UCOM
UNLOCK
SRCERR
LAT2
U714
AD1893
Sample Rate Converter
N/C
BLCK_I
WCLK_I
LR_I
VDD
BKPOL_I
MODE0_I
WCLK_O
LR_O
DATA_O
BKPOL_O
MODE0_O
C736 0.1
C735 0.1
C734 47/25V
R790 47
D-IN DATA (SRC ON)
SRCONDT
TO ASIC
C743 1000p
R795 1K
C733 0.1
R785 100
R786 100
U713A
74VHC74
PAD5
SHORT
C744 1000p
R796 1K
C740 0.1
R789 47K
U715
TC9271
DIGITAL AUDIO I/F
(SPDIF) TRASMITTER
BLOCK
UBDA
IRS
LRCK
BCK
DATA1
DATA2
DEM
COPY
F0
F1
CKS
XI
VSS
VDD
CKA2
CKA1
LBIT
FR32
CTG3
CTG2
CTG1
IS2
IS1
M2
M1
DO2
DO1
DOUT-OFF
TO UCOM
R797 47
ADOUT
TO ASIC
SPDIF OUT
P703
+5V
GND
RXOP T
TX
RXCOA
D-SEL
PH-6
R794 100
FROM UCOM
D-SEL
R864 10K
FROM ATIO
CLKSEL5
MASTER 256FS
DIN 256FS
U716C 74HC02
U716A 74HC02
U716D 74HC02
U716B 74HC02
C741 0.1
R787 22
R788 22
FROM ATIO
RESET-
FROM UCOM
DRESET
FROM ATIO
UBDA
GND
U713B
74VHC74
CONVERTER PCB(4/4)
DIO SECTION